

Mixed Signal Circuit Design and Simulation

Marathon using eSim

FOSSEE, IIT Bombay, along with VLSI System Design Corp.Pvt. Ltd and Redwood EDA is organising this **online Mixed Signal Circuit Design and Simulation**. eSim is an open source alternative to PSpice, Orcad, etc.



Marathon Duration

25 February - 17 March 2022

About Mixed Signal Circuit Design and Simulation Marathon

The purpose of Mixed Signal Circuit Design and Simulation Marathon using eSim is to provide an open platform to students to innovate, create state-of-the-art design and integrate it into real-world applications. This marathon is based on eSim, an open source EDA tool for circuit design, simulation and PCB design, developed by FOSSEE, IIT Bombay.

Why eSim?

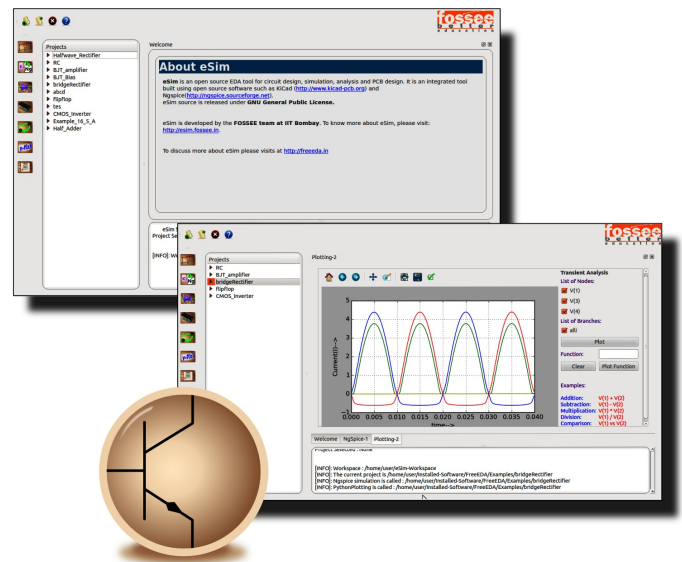
- An Open Source EDA tool
- Use free of cost
- No license needed
- Beneficial for research and SMEs
- Useful for consultants and entrepreneurs
- Switch to other similar software with ease

Benefits

- e-Certificate on successful completion
- Recognition on the FOSSEE Website
- Improved internship/job opportunities

Contact us

contact-esim@fossee.in



For more details, visit <https://hackathon.fossee.in/esim/>



Registration Free



Spoken Tutorial



VLSI System Design

